ABSTRACT

In a de-emphasis circuit designing method for minimizing the number of peripheral components of a video signal processing integrated circuit (IC), a circuit device for determining the level of a reproduced video signal of the de-emphasis circuit is included in the video signal processing IC. The number of components is reduced by designing all circuit elements of the circuit device for determining the level of the reproduced video signal of the de-emphasis circuit so that they are incorporated into the video signal processing IC at the time of design of the video signal processing IC, thereby reducing cost and reducing any deviation in the playback (PB) level due to external component deviation. The video signal processing IC comprises the de-emphasis circuit and a video level setting unit, the latter comprising an amplifier connected to an output of the de-emphasis circuit, a plurality of resistance elements, and a plurality of respective switches for ON/OFF switching of the resistance elements, thereby setting the gain of the amplifier and the level of the reproduced video signal.